

<b>Notice of References Cited</b>	Application/Control No. 10/060,454	Applicant(s)/Patent Under Reexamination MUROOR, SRIKANTH R.	
	Examiner Thomas J. Cleary	Art Unit 2111	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Hill, Frederick J. et al. Computer Aided Logical Design With Emphasis on VLSI. Fourth Edition. 1993. John Wiley & Sons, Inc. ISBN 0-471-57527-5. Pages 54-57.
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